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Serial No.:09/683,091



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

*In re* patent application of

Docket No.: BUR9200000199

William D. Corti

Serial No.: 09/683,091

Group Art Unit: No.: 2184

Filed: November 16, 2001

Examiner: Wilson, Yolanda L.

For: **On-Chip Logic Analyzer**

**RECEIVED**

Mail Stop: Non-final amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

JAN 23 2004

Technology Center 2100

**REPLY UNDER 37 C.F.R. §1.111**

Sir:

In response to the Office Action dated December 8, 2003, Applicants hereby request reconsideration of the above-identified application in view of the following remarks.

Applicants believe that no extensions of time or fees for net addition of claims are required at this time. However, if additional extensions of time are necessary to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 C.F.R. §1.136(a), and any fees required therefor (including fees for net addition of claims) are hereby authorized to be charged to IBM Deposit Account No. 09-0456.